



SHEET 1 OF 1

INFORMATION DISCLOSURE
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Paul Caprioli, et al.FILING DATE
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U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
Jm	0 357 188	07.03.90	EPO				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Jm	IBM Technical Disclosure Bulletin entitled "Deferred decoding", Vol. 27, No. 10B, March 1985 IBM Corp., Pages 6257-6258, XP-000885126.
Jm	Publication entitled "Runahead Execution: An Alternative to Very Large Instruction Windows for Out-of-order Processors", by Onur Mutlu et al., Proceedings of The Ninth International Symposium on High-Performance computer Architecture, IEEE, 2002
Jm	Publication entitled "Beating in-order Stalls with "Flea-Flicker" two-pass Pipelining", by Ronald D. Barnes et al., Proceedings of the 36 th International Symposium on Microarchitecture, IEEE, 2003.

EXAMINER

DATE CONSIDERED

4/25/2006